

## ABSTRACT OF THE INVENTION

An input buffer circuit includes a differential amplifier that receives a input signal and its complement and generates an amplified signal corresponding to the voltage difference between the input signal and its complement. A transfer circuit receives the input signal and generates a transfer signal having the same logical value as the input signal. A control circuit connected to the differential amplifier and the transfer circuit selects one of the amplified signal and the transfer signal for output by enabling either the differential circuit or the transfer circuit.